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INFORMATION INFORM

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	Application No.:	10/665,999
	Filing Date:	Sept. 17, 2003
	First Named Inventor:	John R. Allen
	Group Art Unit:	2124
	Examiner Name:	Unknown
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			U.S.	Patent Documents					
*Examiner Intials		Document Number	Date	Name	Class	Subclass		Date i	
/CN/	1.	6,460,177	10/01/02	Lee	717	146	Appi	opriate	
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							Trans	Translation	
		Document	Date	Country	Class	Subclass	Yes	No	
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/CN/	4.	P. Banerjee, "An Overview of the AccelFPGA Compiler for Mapping MATLAB Programs onto FPGAs", Prith Banerjee – UC Berkeley Talk, Nov. 6, 2002, pp. 1-33							
/CN/	5.	M. Groden, 'Automatic RTL Conversion of DSP Algorithms for a Channelized Wideband Receiver", Int'l Signal Processing Conference, April 2003, Dallas, TX, pp. 1-5							
/CN/	6.	"Filter Design Toolbox" For Use with MATLAB, User's Guide, Version 2, Chapter 5 "Quantization and Quantized Filtering" pp. 5.2-5.26, Chapter 6, "Working with Objects" pp. 6.1-6.10, Chapter 7, "Quantizers and Unit Quantizers, pp. 7.2-7.10, Chapter 8, "Working with Quantized Filters", pp. 8.1-8.16, Chapter 9, "Working with Quantized FFTs", pp. 9.1-9.10, Chapter 10, "Quantized Filtering Analysis Examples", pp. 10.1-10.26, June, 2001							

Examiner:	/Chuong Ngo/	Date Considered:	03/26/2007	·						
* Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication with applicant.										